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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\* *se*

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *se*  
 JAPAN 2003-071660 03/17/2003

IF REQUIRED, FOREIGN FILING LICENSE GRANTED  
 \*\* 05/29/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>kan</i> Examiner's Signature	<input type="checkbox"/> no <input type="checkbox"/> no <i>se</i> Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 17	TOTAL CLAIMS 12	INDEPENDENT CLAIMS 1
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ADDRESS  
 004372  
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TITLE  
 Semiconductor memory device with shift register-based refresh address generation circuit

FILING FEE	FEES: Authority has been given in Paper	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of
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